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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Michael Y. Lai

Serial No.: 10/674,364

Filed: September 29, 2003

For: Method and Apparatus for
Bit Field Optimization

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Art Unit: 2192

Examiner: Isaac Tuku Tecklu

Docket: ITL.1482US
P16116

Assignee: Intel Corporation

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

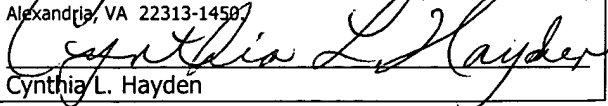
REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed November 1, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: January 31, 2007

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Cynthia L. Hayden